Poznan University of Technology
Faculty of Electronics and Telecommunications

European **C**redit **T**ransfer **S**ystem

Title		Code
Microprocessors		**1018011410108100069**
Field		Year / Semester
Electronics and Telecommunications		**2 / 4**
Specialty		Course
-		**core**
Hours		Number of credits
Lectures: **2** Classes: **1** Laboratory: **2** Projects / seminars: **-**		**0**
	Language	
	polish	

Lecturer:

- dr hab. inż. Paweł Szulakiewicz prof. PP
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Faculty:

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Status of the course in the study program:
- Obligatory, 2nd year

Assumptions and objectives of the course:
- To teach the microprocessor technique and give a thorough description of several Intel microcontrollers,microprocessors and various microprocessor system elements.

Contents of the course (course description):
- Introduction to the microprocessor technique.
- Intel microcontroller family ? MCS-51/251, programming in assebly language, system elements.
- Intel microprocessors - architecture, instructions, programming. The 8086/8088, 80286 hardware specifications. Memory. I/O interface. Interrupts. DMA. Coprocessor.
- The 80386 and 80486 microprocessors.
- The Pentium and Pentium Pro microprocessors.

Introductory courses and the required pre-knowledge:
-

Courses form and teaching methods:
-Lectures, problem solving, laboratory

Form and terms of complete the course - requirements and assessment methods:
- Exam

Basic Bibliography:
-

Additional Bibliography:
-

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